Click Senor Hub:

Dr. C:

Fix date of milestones for senior design day

Kemp:

How are you going to build your software architecture?

How are you going to marginalize your code?

How are you going to drive the MUX from the KL46Z?

Are you sure you can get 4 states out of..?

Analog lines are not MUXING yet?

Stapleton:

If your getting 5V in do you need a 5V regulator?

Specs of KL46Z, Diode Circuit,

Are you just going to be test pointing signals from KL46 or signals from SPI interface?

Hinkle:

Are you planning to try to mock this board up or simulate it?

Mcllelan:

More detail in slides than in functional spec,

Error Handling:

What are potential errors?

Figure out the boundaries of the performance.

State Diagram: Project seems more complicated at first, a lot of potential error issues

Document Table 1: (comes after table 9) Alignment Matrix,

Interfacing a device not designing a device

Aslan:

When you create testing, you must have values that you know

Biggest Issue: Testing

PCB 2 layer-simulate first, before you build it

Go through the spec and fix any errors